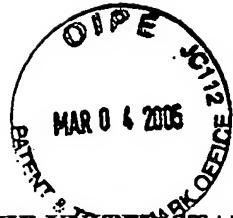


NETL.001DV3



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Jayesh R. Bhakta et al.

Appl. No. : 10/765,488

Filed : January 27, 2004

For : ARRANGEMENT OF INTEGRATED
CIRCUITS IN A MEMORY MODULE

Examiner : Ly D. Pham

Group Art Unit 2827

05/24/05
OK to enter!
(L)

AMENDMENT AFTER NOTICE OF ALLOWANCE UNDER 37 C.F.R. § 1.312

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. § 1.312, Applicants are submitting herewith this Amendment After Notice of Allowance. Applicants respectfully request that the following amendments be entered on the recommendation of the Primary Examiner, and approval by the Commissioner.

Amendment to the Claims: Begin on page 2 of this Response.

Remarks: Begin on page 6 of this Response.